

# ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

**Title of  
Invention**

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN  
AN RF POWER AMPLIFIER

Submission Type : Information Disclosure  
Statement

Application Number:

10/743221



EFS ID:

95058

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-fee-sheet	SILP63-10-20-05-usfees.xml	1467	2005-10-20
us-fee-sheet	us-fee-sheet.xml	25930	2005-10-20
us-fee-sheet	us-fee-sheet.dtd	11968	2005-10-20
us-ids	SILP63-10-20-05-usidst.xml	3204	2005-10-20
us-ids	us-ids.dtd	7763	2005-10-20
us-ids	us-ids.xml	12026	2005-10-20
package-data	SILP63-10-20-05-pkda.xml	2249	2005-10-20
package-data	package-data.dtd	27025	2005-10-20
package-data	us-package-data.xml	19263	2005-10-20
Total files size		110895	

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